

Fig. 1 V_{CC} (fixed)

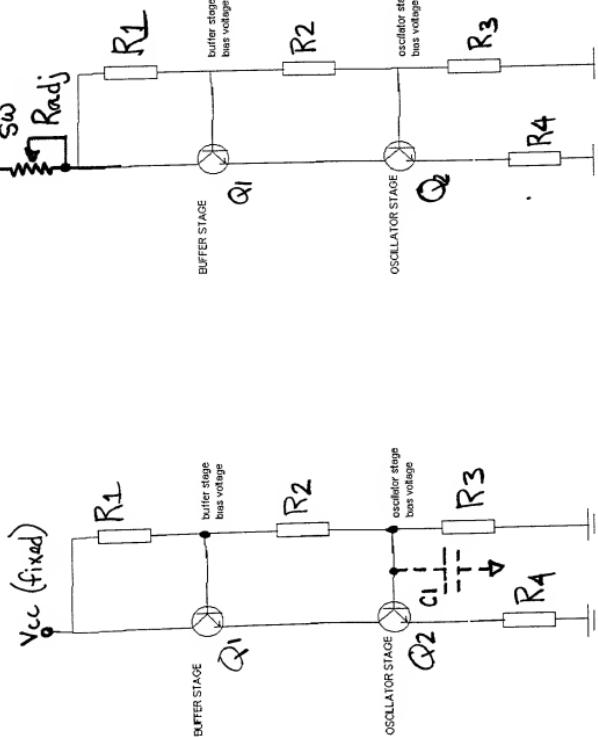


Fig. 1
(PRIOR ART)

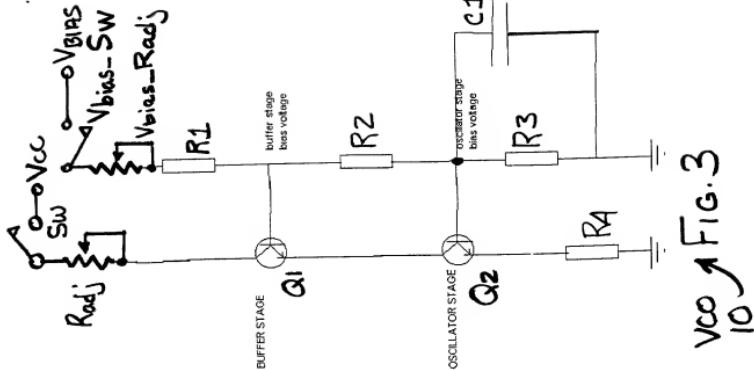
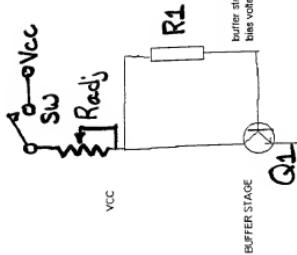
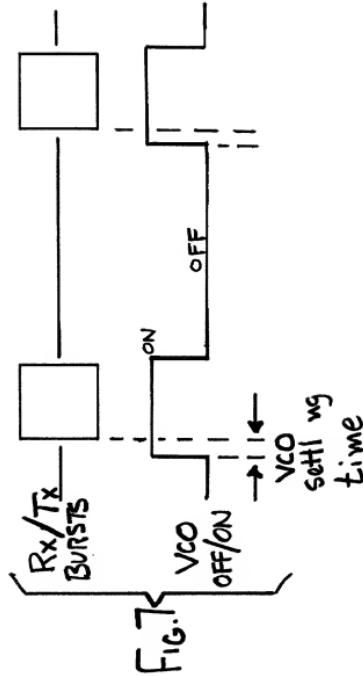


Fig. 2
 V_{CO}

Fig. 3
 V_{CO}



$V_{bias-Sw}$
 $V_{bias-Radj}$
 $V_{bias-Radj}$



VCO
 IO 1

R_4

C_1
 oscillator stage bias voltage

R_3

Q_2
 oscillator stage bias voltage

R_2

Q_1
 buffer stage bias voltage

R_1

S_{adj}

V_{CC}

V_{CC}

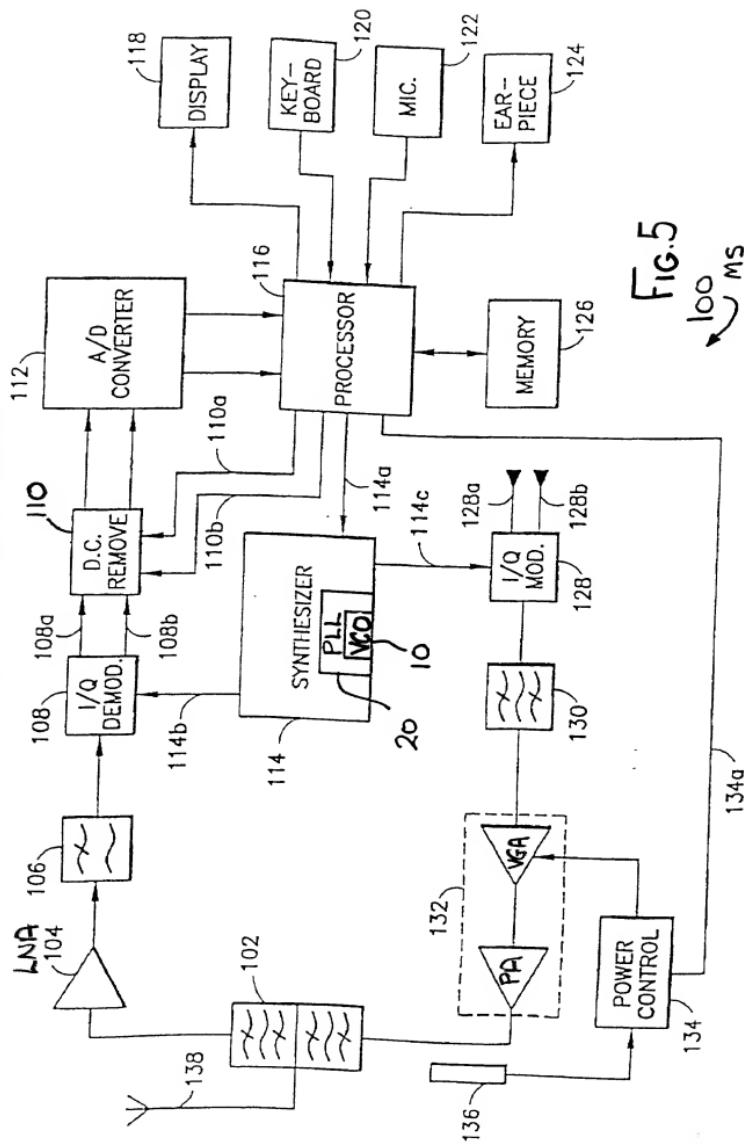


Fig.5
100 ms

I/Q DEMOD 108

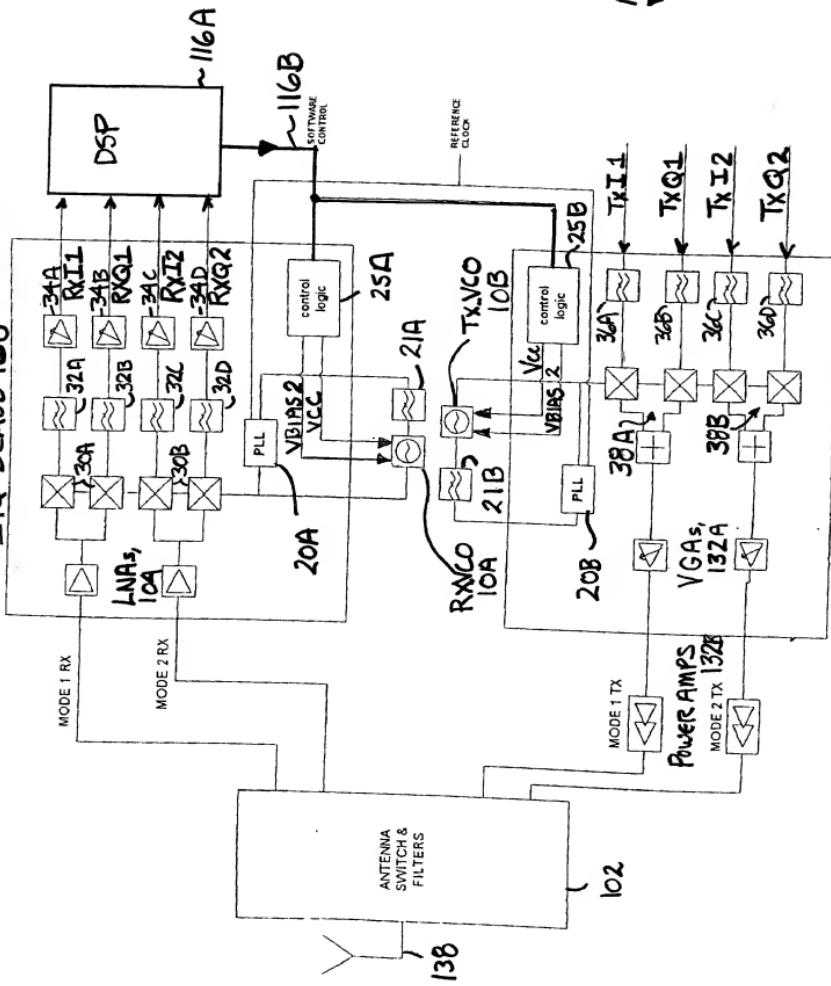


FIG. 6
PART OF
MS100

I/Q MOD 128

